ABSTRACT OF THE DISCLOSURE

A semiconductor device includes first, second, third and fourth resistor elements. The first to fourth resistor elements have first ends commonly connected to a first node, and operate in one of first and second operation modes which are switchable. The first and second resistor elements have second ends connected to a second node and a third node, respectively. The third and fourth resistor elements have second ends connected to a fourth node and a fifth node via a first switch and a second switch, respectively. The first and second switches are opened in the first operation mode, and are closed in the second operation mode.